

# PAPALA PADMAJA

## Engineer

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## SUMMARY

I am an Electronics and Communication Engineer with 8 months of experience in the automotive domain. I successfully completed an embedded systems training program and have practical skills in programming languages, microcontrollers, and software tools. Noteworthy projects include HMI development for car head units and research on reversible logic gates, contributing to advancements in quantum computing.

## EXPERIENCE

### Engineer

#### L & T Technology Services

03/2024 - 12/2024 Mysuru

A leading engineering and technology services company.

- Worked on software release process
- Fixed implementation related issues
- Addressed reported bugs
- Involved in code analysis tools bugs in settings and multimedia modules

## EDUCATION

### B.TECH(ECE)

#### Gouthami Institute Of Technology & Management for Women

08/2017 - 05/2021 JNTUA University

### INTERMEDIATE

#### Sri Chaitanya Junior College

08/2015 - 05/2017 State board

### S.S.C

#### Z. P. H. School

08/2014 - 05/2015 State board

## SKILLS

C programming

C++

Linux

Embedded C

UART

SPI

I2C

CAN

LPC2129-ARM7(micro-controller)

Visual Studio

Windows

JIRA

GCC

## PROJECTS

### HMI Development on Infotainment System

01/2024 - 05/2024 Mysuru

Development of HMI applications for automotive infotainment system.

- Developed Human machine Interface Applications for 4-wheeler car head units
- Enhanced applications like settings and multimedia based on specifications
- Managed general settings screens modules

### College project:

04/2021 Proddatūr, India

Implementation of high speed low power combinational and sequential circuits using reversible Logic gates

- Reversible logic has presented itself as a prominent technology which place an imperative role in quantum computing.

## COURSES

### Vector India

Completed training in the field of Embedded Systems from Vector India Pvt.Ltd. (Bangalore).